



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

JOHN F. ZUMKEHR, ET AL.

Application No.: 10/714,075

Filed: November 14, 2003

For: **Internal Voltage Reference for Memory Interface**

Art Group: 2819

Examiner: Daniel D. Chang

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

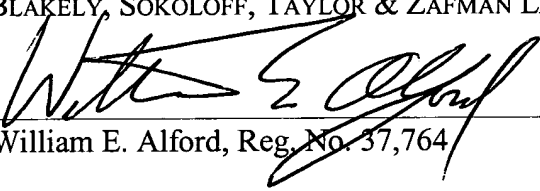
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Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: September 26, 2005

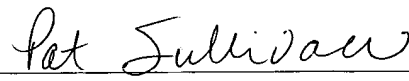


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Date

Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet

1

of

2

Complete if Known

Application Number	10/714,075
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Filing Date	November 14, 2003
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First Named Inventor	John F. Zumkehr
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Art Unit	2819
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Examiner Name	Daniel D. Chang
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Attorney Docket Number	42P17976
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U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]Examiner
Signature

Date	
Considered	

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Complete if Known	
				Application Number	10/714,075
Sheet 2 of 2				Filing Date	November 14, 2003
				First Named Inventor	John F. Zumkehr
				Art Unit	2819
				Examiner Name	Daniel D. Chang
				Attorney Docket Number	42P17976

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T ²
		JEDEC STANDARD, DDR2 SDRAM SPECIFICATION, JESD79-2, September 2003, JEDEC Solid State Technology Association, Electronic Industries Alliance		
		JEDEC STANDARD, Double Data Rate (DDR) SDRAM Specification, JESD79C (Revision of JESD79B), March 2003, JEDEC Solid State Technology Association, Electronic Industries Alliance		

Examiner Signature		Date Considered	
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